Figure 6.33 Rail-to-rail folded-cascode input stage.
Figure 6.34 Schematic of the NE5234 bias circuit.

Figure 6.35 Simplified schematic of the NE5234 bias circuit.
Figure 6.36 Simplified schematic of the NE5234 input stage.
Figure 6.37 Schematic of the NE5234 second stage. Node Out is the output of the third stage (shown in Figure 6.39).
Figure 6.39 Schematic of the NE5234 output stage.
Figure 6.41 Simplified schematic of the NE5234 output stage with its bias circuit.