

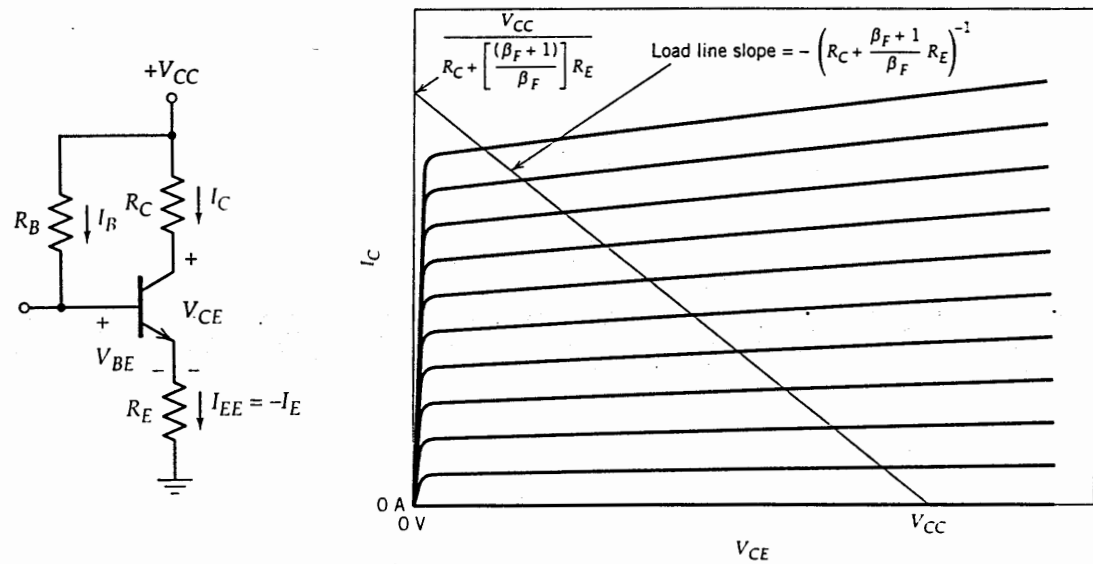
TAKE HOME QUIZ 1

ISSUED: Monday October 25, 1999

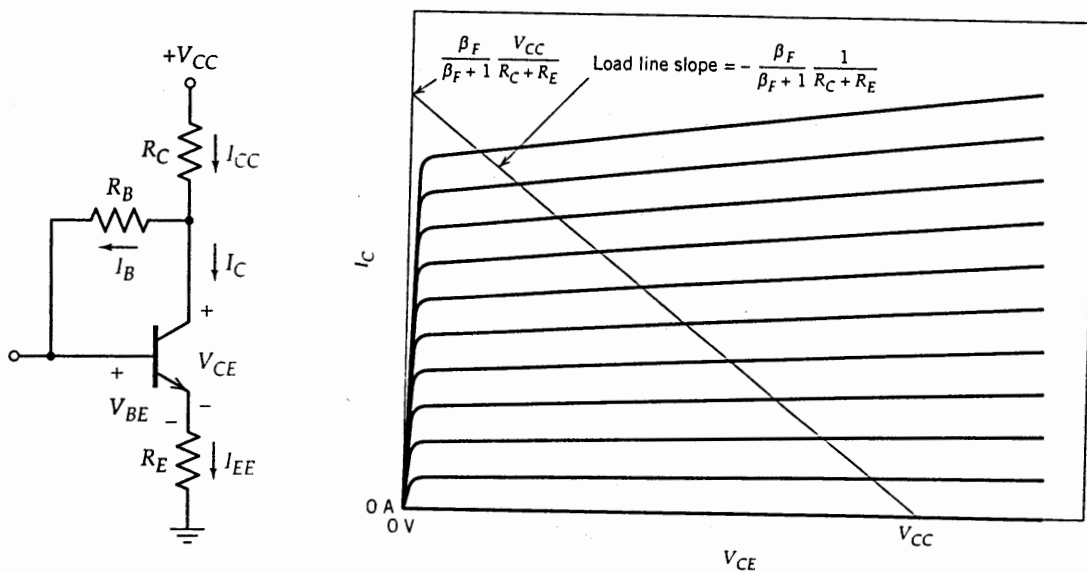
DUE: Monday November 1, 1999

Given: Consider the following bias configurations for an npn BJT, together with the corresponding load lines plotted on the I_C - V_{CE} graphs. Assume Transistor is operating in the active region and in the active region we can use $I_C = \beta I_B$.

Required: Verify that the load intercepts and slopes are correctly given in the graphs. You must solve using equivalent circuits. Show all your steps.



fixed-bias emitter feedback transistor circuit.



Fixed-bias circuit with collector and emitter feedback.